

Notice of References Cited

Application/Control No.

10/621,067

Applicant(s)/Patent Under

Reexamination

FARKAS ET AL.

Examiner

KENNETH TANG

Art Unit

2195

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,006,248	12-1999	Nagae, Toshihide	718/105
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kumar et al. "Processor Power Reduction Via Single-ISA Heterogeneous Multi-Core Architectures", IEEE Computer Architecture Letters, Vol. 2, April 2003, pages 1-4
	V	Kumar et al. "Processor Power Reduction Via Single-ISA Heterogeneous Multi-Core Architectures", IEEE Computer Architecture Letters, Volume 1, Issue 1, January 2002, Abstract.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))
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